AMENDMENT UNDER 37 C.F.R. 1.116 – EXPEDITED PROCEDURE

Serial Number: 10/758,055

Filing Date: January 15, 2004

Title: METHOD FOR REDUCING SOCKET WARPAGE

Assignee: Intel Corporation

REMARKS

Page 5

Dkt: 884.B24US2 (INTEL)

This responds to the Office Action mailed on September 23, 2005.

Claims 17, 20, and 23 are amended, and claims 1-9 and 26-30 are canceled. Claims 17-25 are pending in this application.

§112 Rejection of the Claims

Claims 17-25 were rejected under 35 USC § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which Applicant regards as the invention.

The rejection was based upon the use of the term "ambient conditions" in claims 17 and 20. Applicant believes that entry of the proposed amendment to those claims, deleting reference to "ambient conditions," is responsive to and moots the prior rejection. Reconsideration of the amended claims and withdrawal of the rejection is requested.

§102 Rejection of the Claims

Claims 17-25 were rejected under 35 USC § 102(b) as being anticipated by Mori et al. (U.S. 5,482,471).

The Mori patent relates to a socket for testing integrated circuit packages. It has a lower block 10 and an upper block 20 and a spring biased slide block 40 slidably mounted for reciprocating movement in a recess provided between the upper and lower blocks. The slide block 40 has a plurality of apertures 41 which open and close contacts 11 upon movement of the cover member 30 by the use of a contact part opening and closing element 50, converting movement of the cover 30 into sliding action of the slide block 50. Mori does not address the issue of warpage reduction of socket connectors.

In the Office Action it is contended in support of the rejection of claim 17 that Mori shows "forming at least one groove 14 in a socket housing 10 contiguous to a surface mount region for an electrical device 40..." Applicant respectfully traverses the Office Action's characterization of Mori. The Office Action fails to explain how it is that what Mori consistently

Serial Number: 10/758,055

Filing Date: January 15, 2004

Title: METHOD FOR REDUCING SOCKET WARPAGE

Assignee: Intel Corporation

refers to as "slide block 40" can be construed to be "an electrical device 40" as the Office Action asserts¹.

Page 6

Dkt: 884.B24US2 (INTEL)

The Office Action continues, stating "...inserting a rigid bar 50 into the groove 14 to thereby ensure that the surface mount region is flat and remains flat..." Again the Office Action fails to explain how it is that the recited structure is in any way shown by Mori as functioning to keep a "surface mount region" flat (in fact, the Office Action never explains what part of Mori is the "surface mount region"). Applicant's understanding of Mori is that grooves 14 receive lever arms 52 (and 51) of the "contact parts opening and closing mechanism 50.²" Mori goes on to describe a mechanism that slidably moves a slide block 40 horizontally as cover mechanism 30 is moved vertically. Mori has no text or drawing that shows or suggests that closing mechanism 50 necessarily acts to "ensure that the unspecified surface mount region is flat and remains flat" as amended claims 17 and 20 require.

As shown in the specific instances recited above, the Office Action fails to make out a *prima facie* case of anticipation since it has failed to show that Mori anticipates each and every element of rejected claim 17 arranged as called for in that claim.

Additionally, dependent claims 18 and 19 are not anticipated since they add additional features to claim 17 which, as shown above, is distinguishable from the cited Mori patent.

The Office Action also fails to establish a *prima facie* case of anticipation by Mori as to independent claims 20 and 23, merely stating "Limitations of claims 20-25 are also satisfied as the above discussion." Since anticipation was not shown in the specific parsing of claim 17 relative to Mori, the Office Action necessarily fails to show anticipation as to claims 20 and 23 and their dependent claims 21-22 and 24-25 which the Office Action did not even attempt to apply to Mori. The Office Action fails to meet the burden of showing how Mori anticipates each and every element of the claims.

Reconsideration and allowance of the pending claims is respectfully requested.

1

¹ Office Action, page 4, first full paragraph.

² Mori Col 6, lines 34-35

Page 7 Dkt: 884.B24US2 (INTEL)

Serial Number: 10/758,055 Filing Date: January 15, 2004

Title: METHOD FOR REDUCING SOCKET WARPAGE

Assignee: Intel Corporation

Conclusion

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 373-6970 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

TIM A. RENFRO ET AL.

By their Representatives, SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Attorneys for Intel Corporation P.O. Box 2938 Minneapolis, Minnesota 55402

(612) 349-9592

Date 2 1 13, 7066

Charles E. Steffey

Reg. No. 25,179

<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 23rd day of <u>February</u>, 2006.

Name

Signature